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| **Priority** | **Agreed Start Date** | **Ref** | **Task** | **Development Requirement** | **Additional Comments** | **Estimated Completion (Hrs)** | **Developer’s comment**  **Tester’s comment** |
|  | Aug/1/2019 |  | ZB-HW Verification | Verify new ZB-HART/ZB-ANA/ZB-485 board before new MODBUS firmware development | * Power : OK * JTAG : OK * ISP : OK * Flashing : OK | 8 | Completed (Aug/1)  Completed and Tested Fine |
|  | Aug/2/2019 |  | ATMEGA 1281 Porting | Port ZB-ANA/ZB-485 base firmware to new ATMEGA1281 | * ADC/MCU/Watchdog register name change * Timer Pin & Setup change * UART vector name change * EEPROM register name change * External ADC clock change * External 2 Port UART clock change * External 2 port UART memory map change | 24 | Completed(Aug/6)  Completed and Tested Fine |
|  | Aug/7/2019 |  | Common MODBUS RTU Slave Stack | Develop common MODBUS RTU slave stack for ZB-ANA/ZB-HART/ZB-485 |  | 80 | Completed(Aug/27) |
|  | Aug/12/2019 |  | ZB-HART/SC Radar integration | Integrate & verify interworking of ZB-HART and SC Radar | * Tested and sent comments to GODA * ZB-HART firmware modified to slow down polling rate * HART link is quite OK. | 24 | Completed(Aug/14)  Completed and Tested Fine |
|  | Aug/28/2019 |  | MODBUS RTU Slave Stack Driver development, integration, Verification | Develop MODBUS RTU slave stack driver task, Integrate, and verify workings of developed MODBUS RTU Slave stack | * Integrated into ZB-ANA * Integrated into ZB-485 * Integrated into ZB-HART * All the boards/firmware passed 12 hour endurance test at 40ms polling rate with zero error | 24 | Completed(Aug/30)  Completed and Tested Fine |